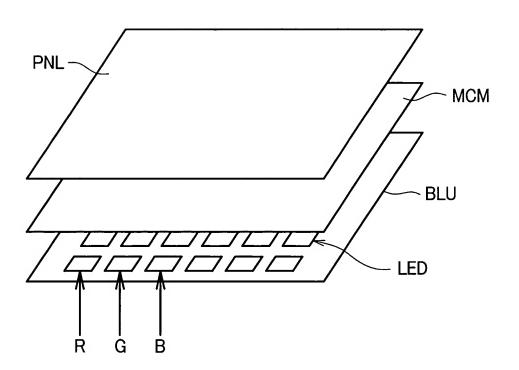
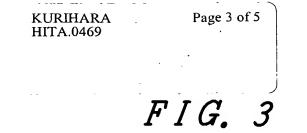
BLU Drive Voltage B Subunit B x0 x0 xb x0 x0 x0 x0 x0 x0 x0 x0/x0 x0 xd x0 x0 xb x0 Brightnes Information B Subunit A XO XO XO 20 20 20 80 80 80 XO XO XO 80 20 20 FIG. 1 Drive Voltage A Information **Brightness** M Pixel Block A Pixel Block B (Corresponding to Subunit A) to Subunit B) SAM to Display Drive Circuit

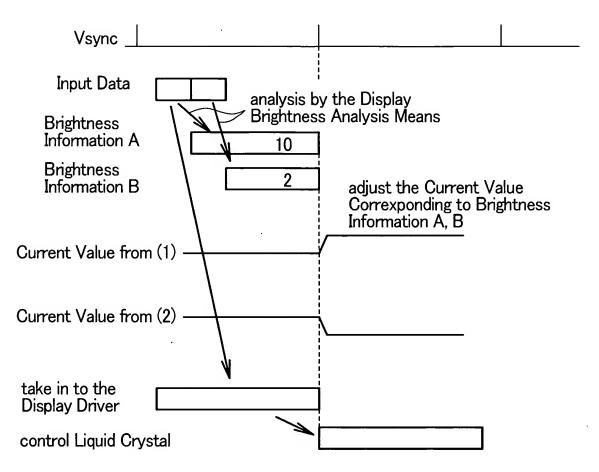
Vsync _

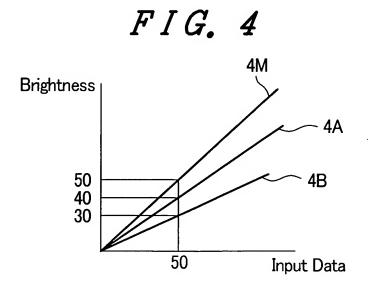
Input Display Data

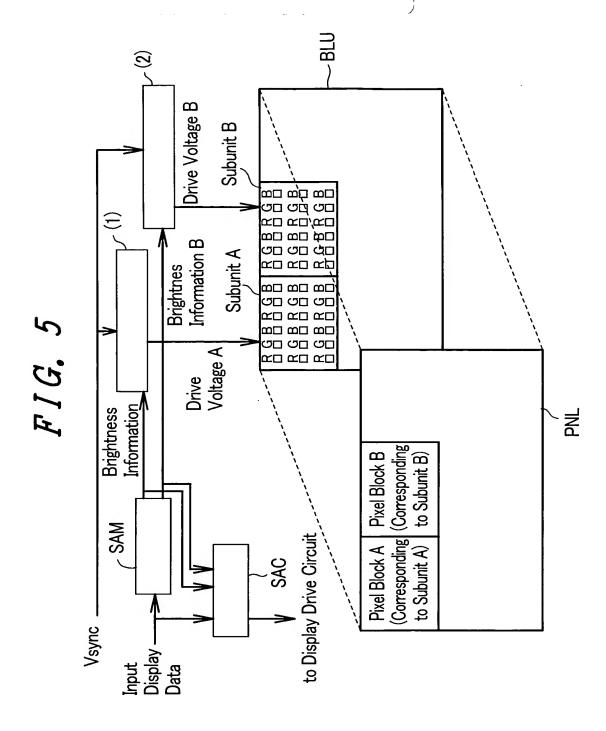
F I G. 2











F I G. 6

